



MULTILAYER CIRCUIT BOARD

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FIG. 1A

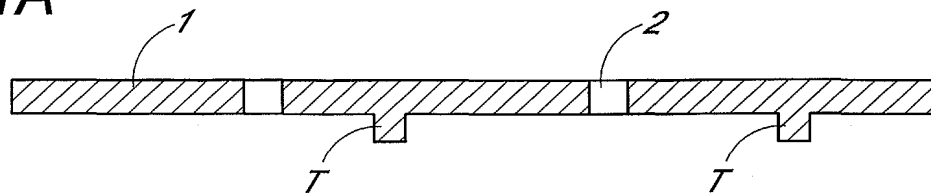


FIG. 1B

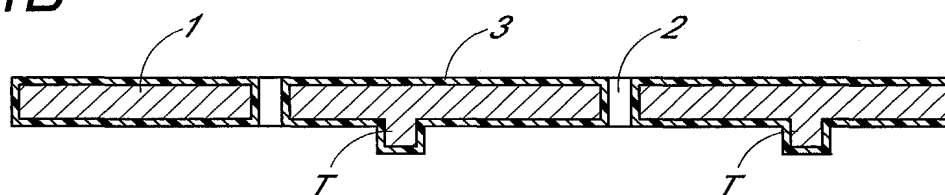


FIG. 1C

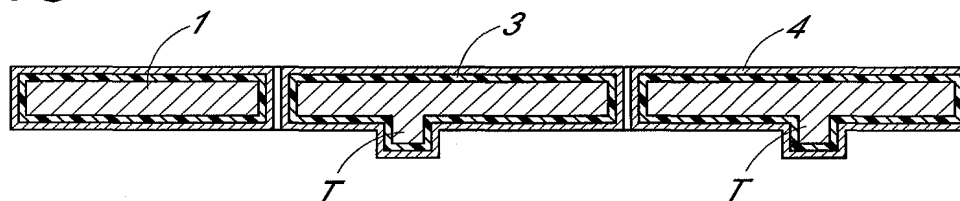


FIG. 1D

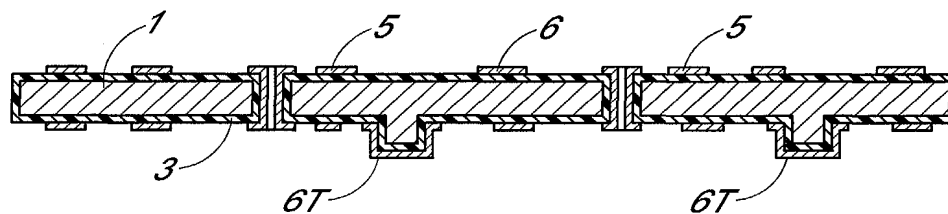


FIG. 1E

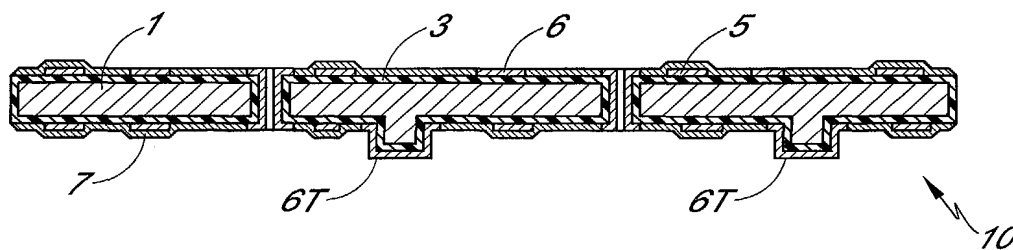




FIG. 2

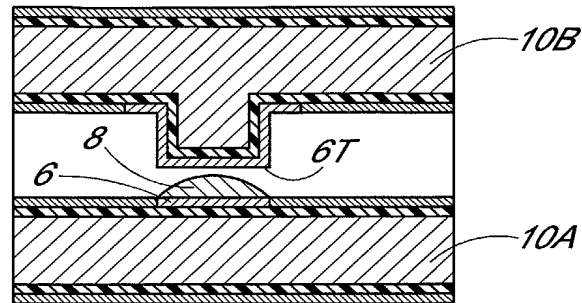


FIG. 3A

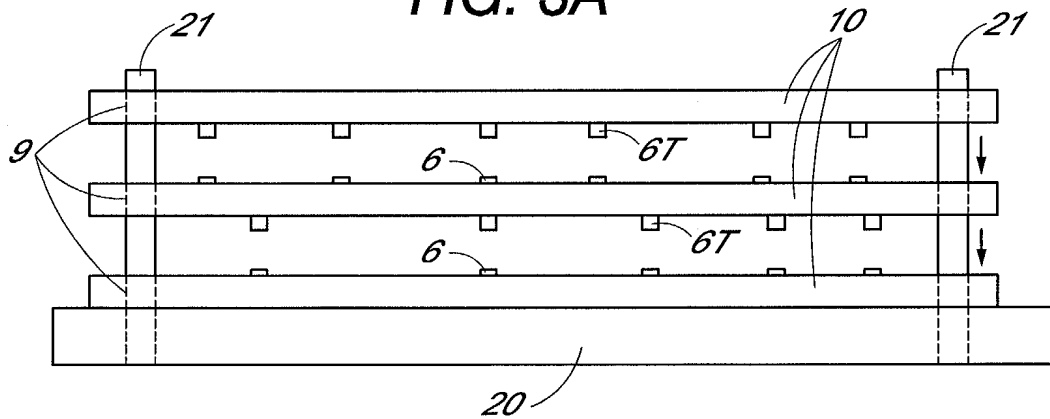


FIG. 3B

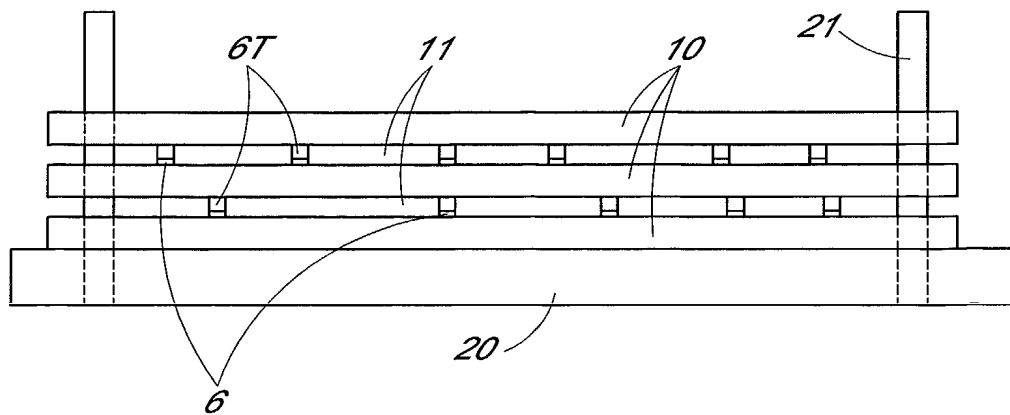




FIG. 4A

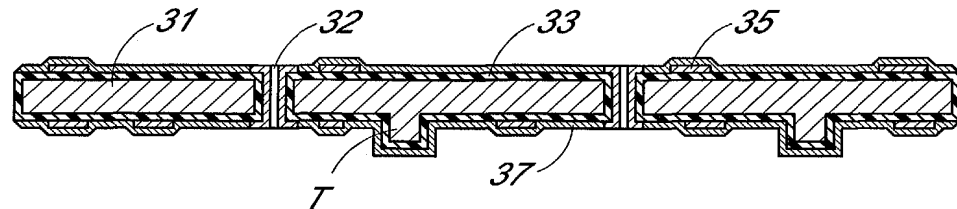


FIG. 4B

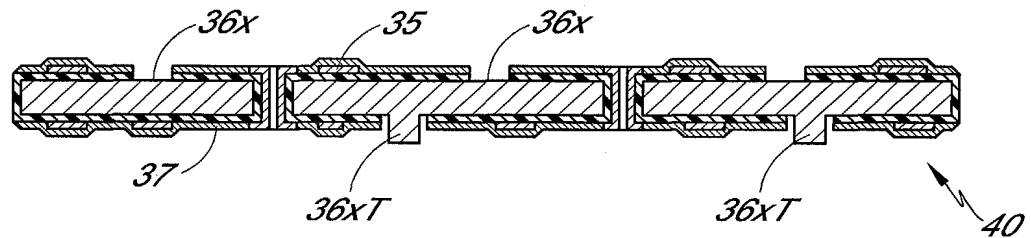


FIG. 4C

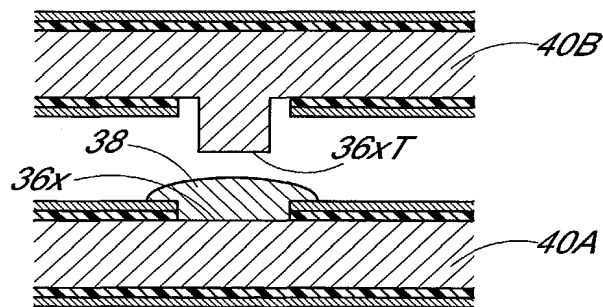




FIG. 5A

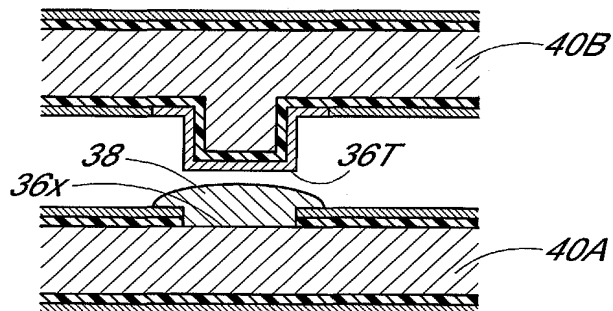


FIG. 5B

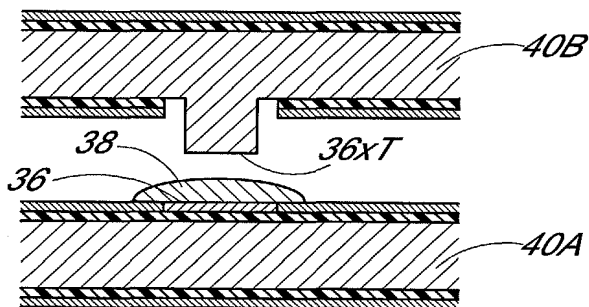


FIG. 10

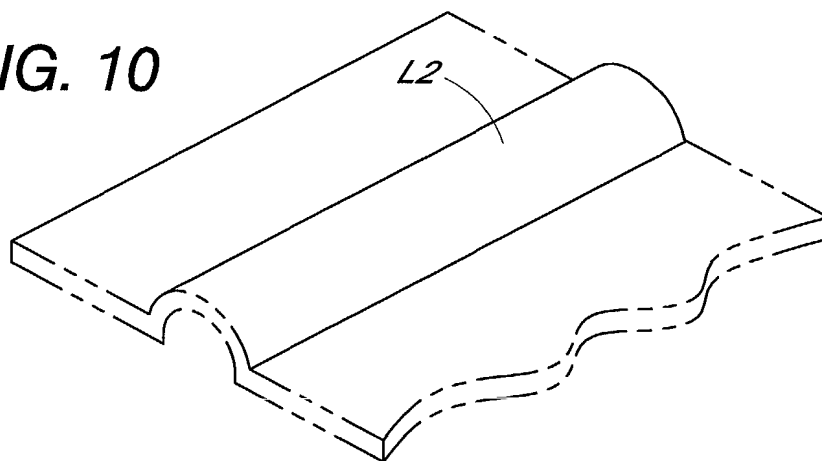




FIG. 6

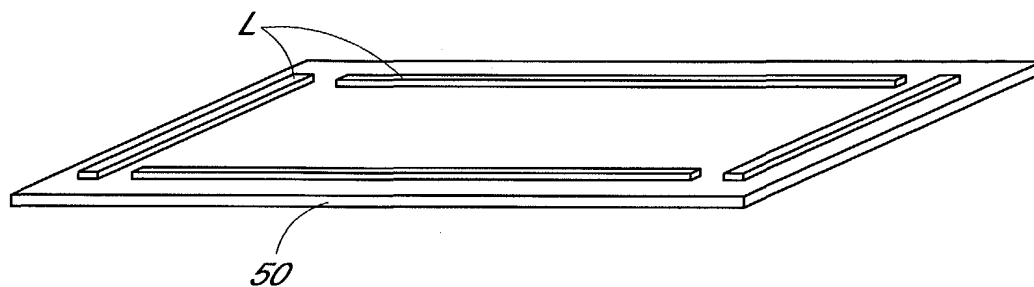


FIG. 7A

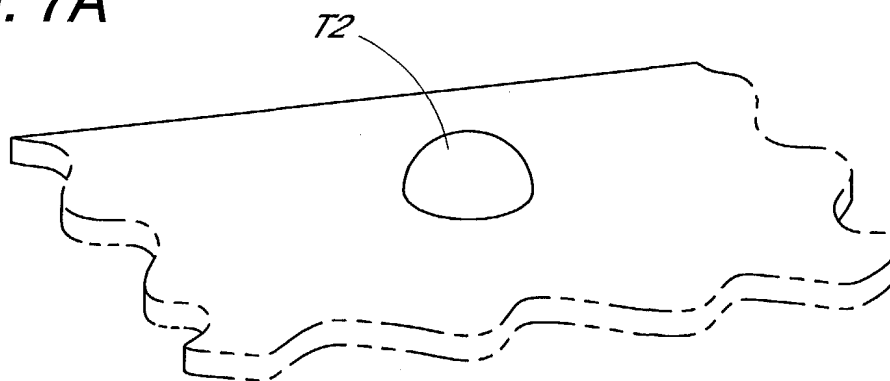
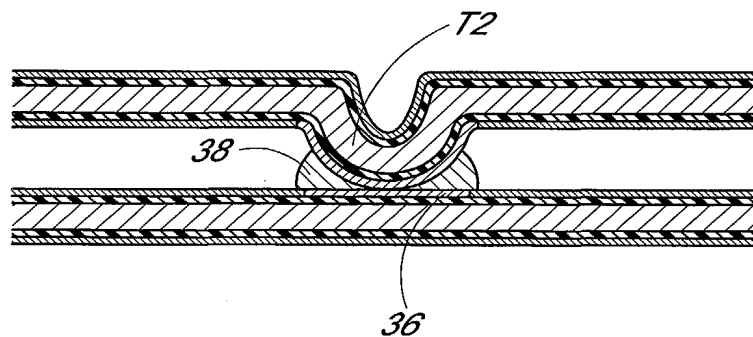


FIG. 7B





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FIG. 8A

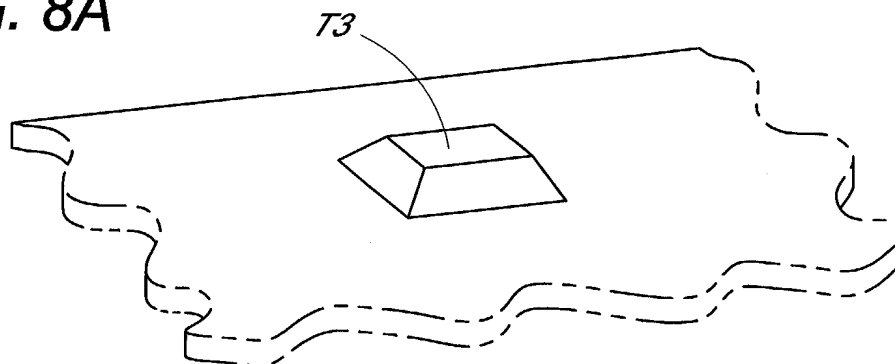


FIG. 8B

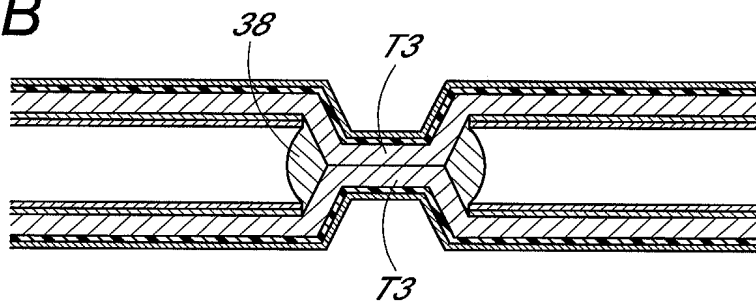


FIG. 9A

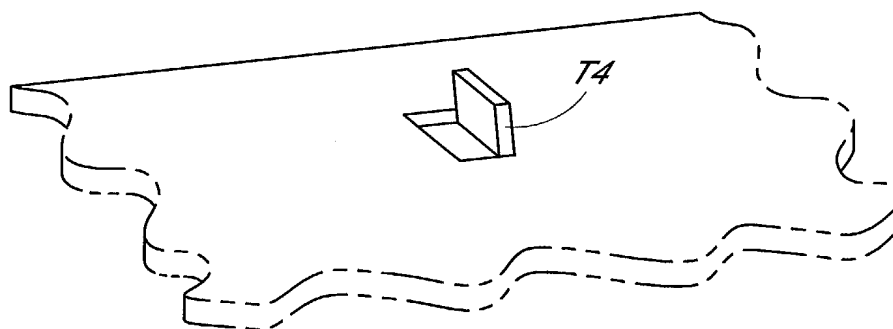


FIG. 9B

